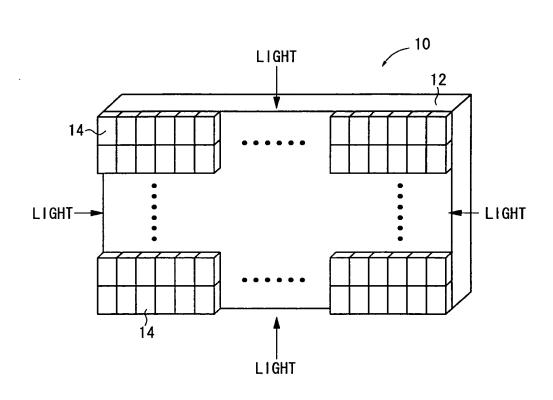
Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

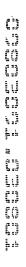
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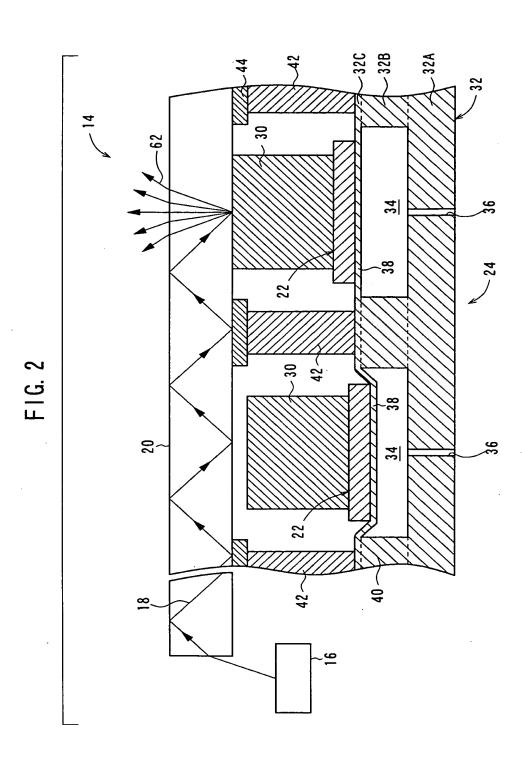
FIG. 1



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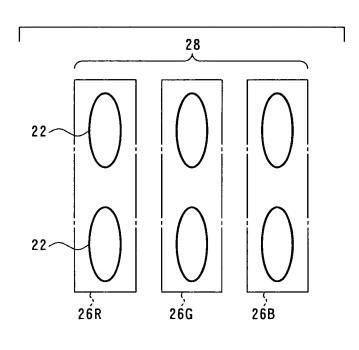
Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

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FIG. 3



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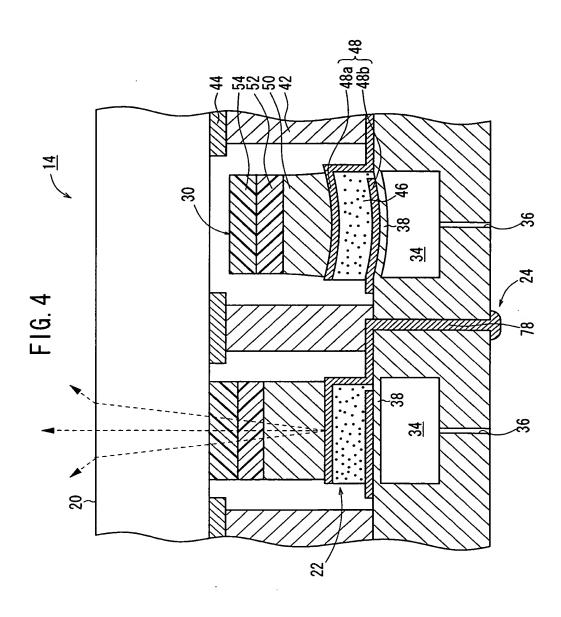
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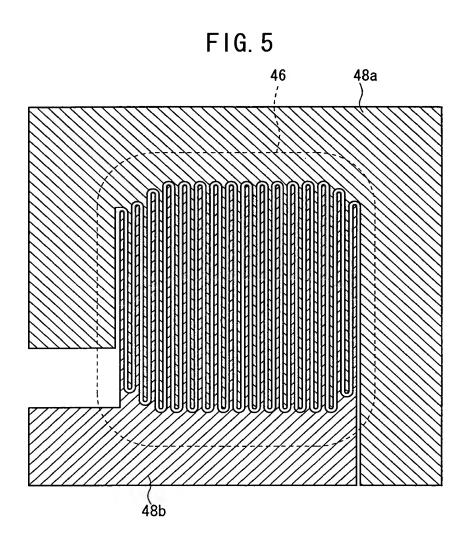
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Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

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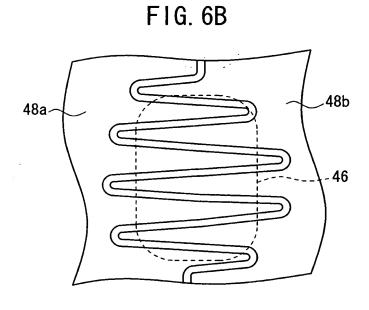
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FIG. 6A

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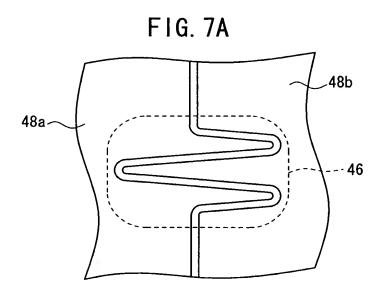
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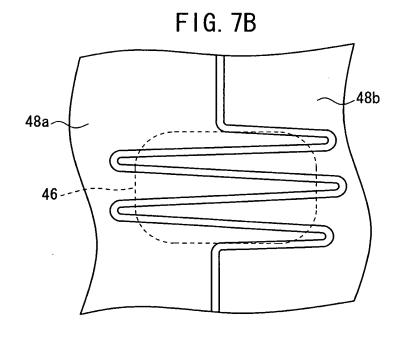


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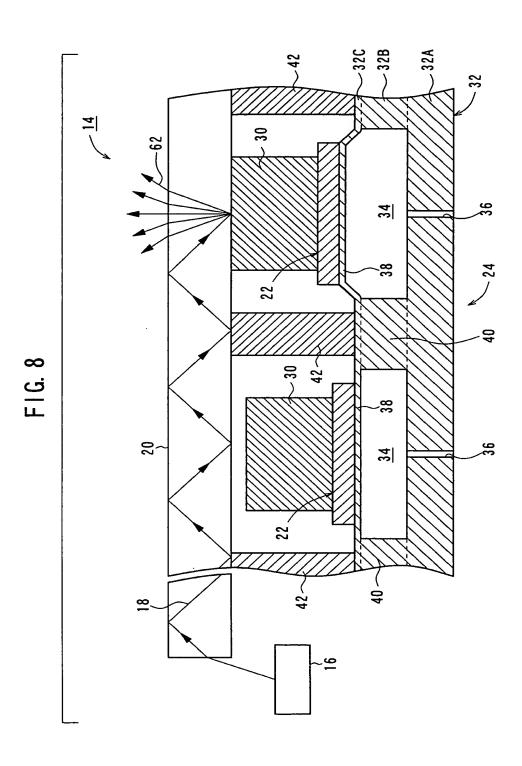
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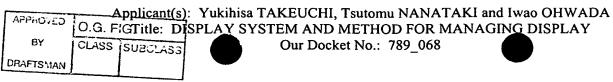


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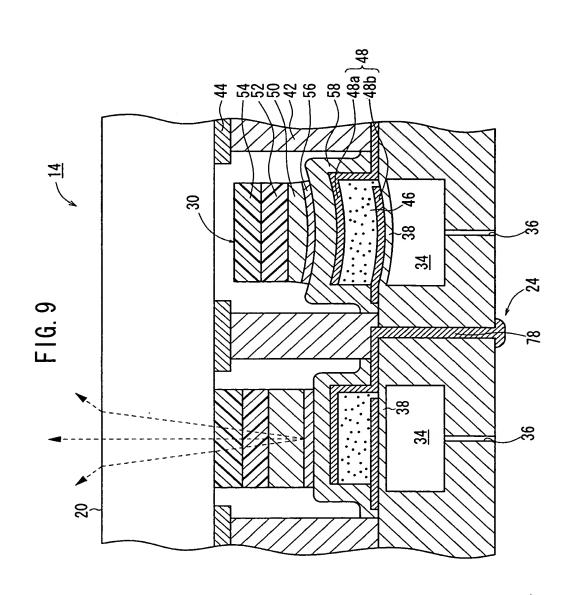


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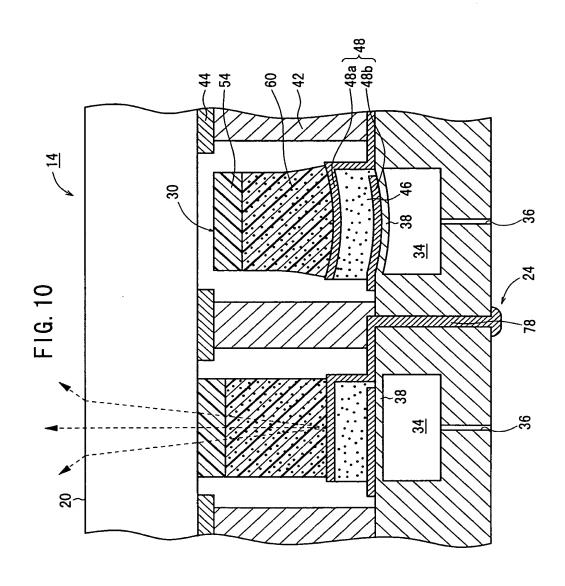
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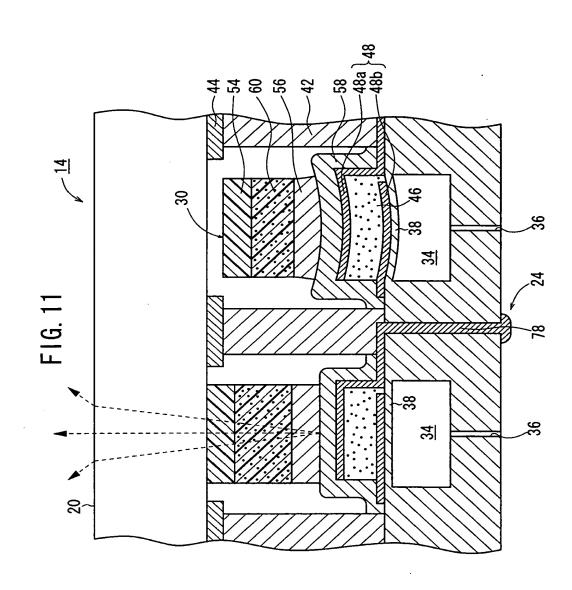
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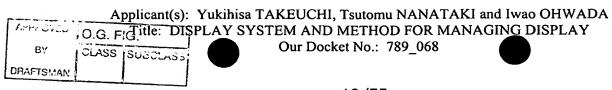


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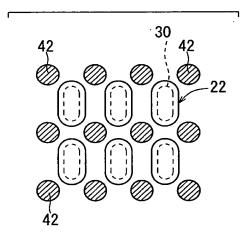
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FIG. 12



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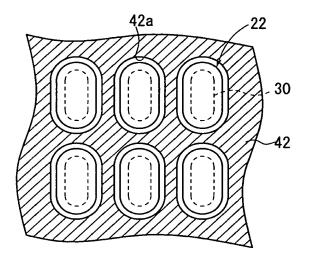
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FIG. 13



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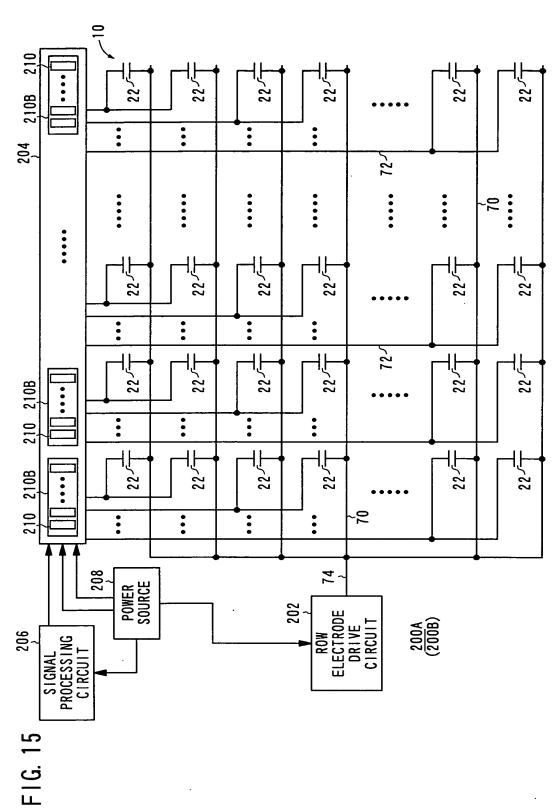
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## FIG. 14

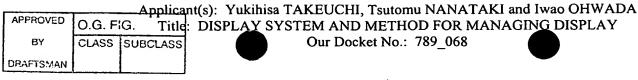
OFFSET POTENTIAL	ON SIGNAL	OFF SIGNAL
OITSET FOIENTIAL	0 V	60V
10V	-10V (LIGHT EMISSION)	50V (LIGHT OFF)

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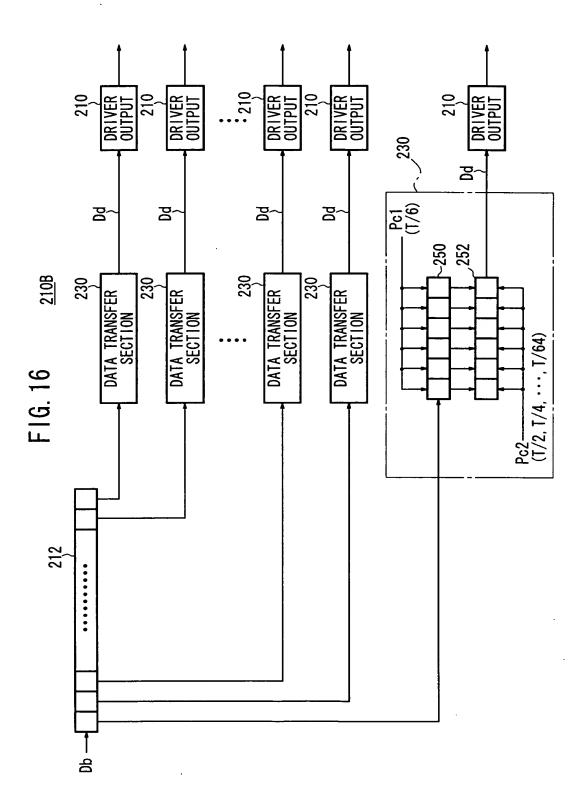
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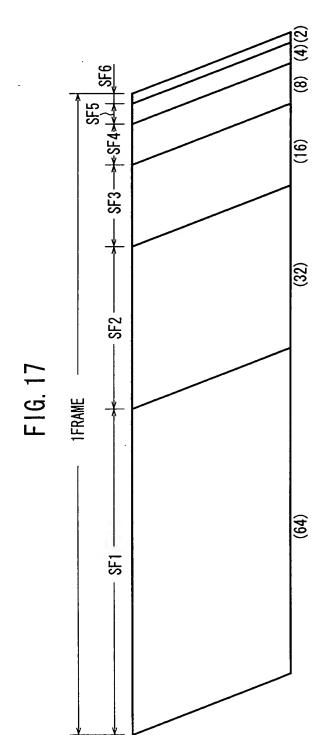


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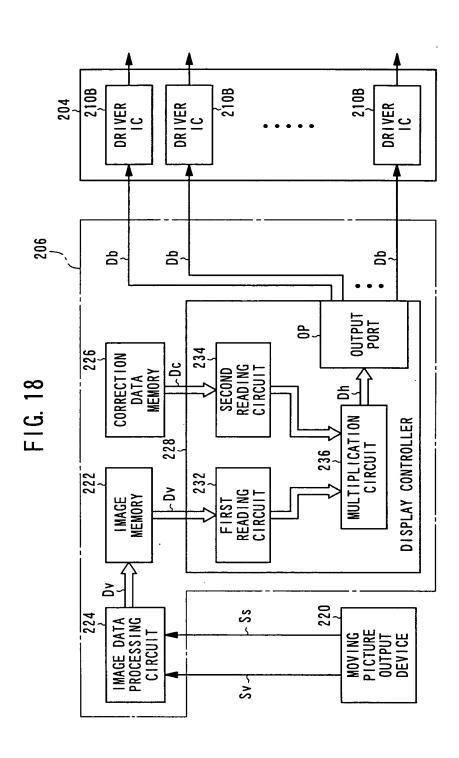
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## FIG. 19

OFFSET POTENTIAL	ON SIGNAL	OFF SIGNAL
OIT SET TOTENTIAL	٥٧	60V
0V	0 V	60V

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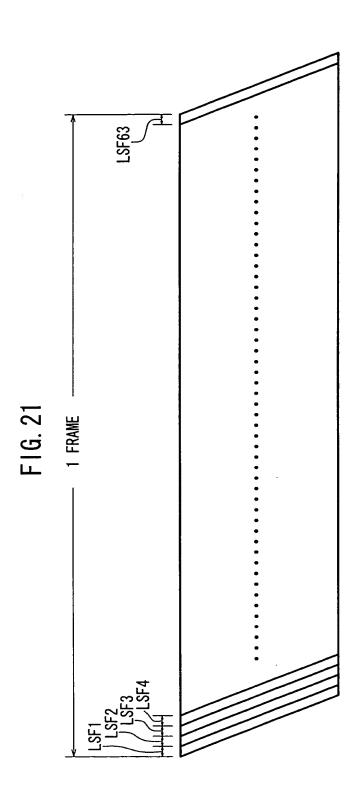
FIG. 20

OFFSET POTENTIAL	ON SIGNAL	OFF SIGNAL
OFFSET POTENTIAL	60V	0 V
50V	-10V	50V

APPROVED O.G. FIG. Title: DISPLAY SYSTEM AND METHOD FOR MANAGING DISPLAY

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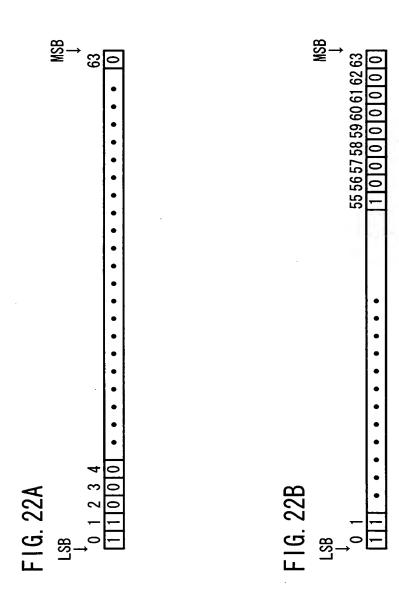
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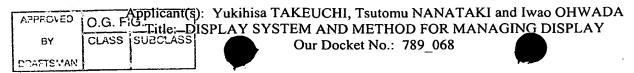
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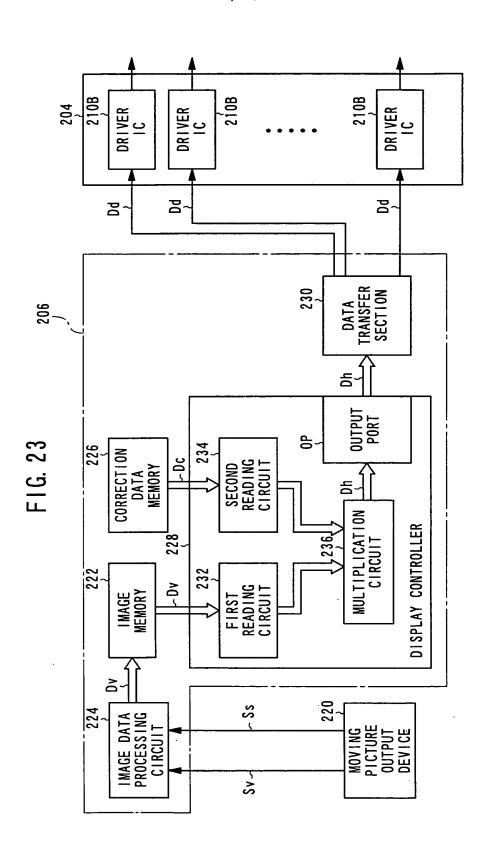
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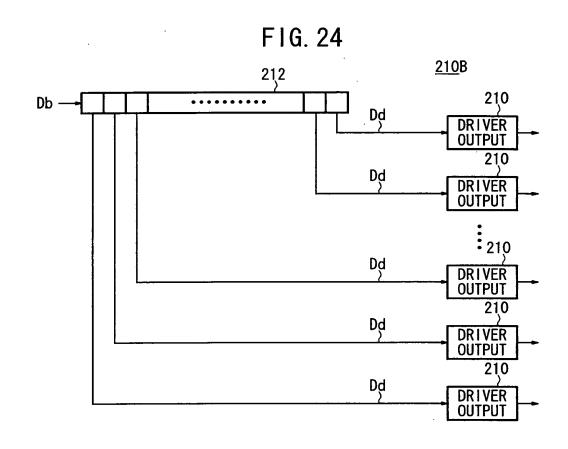
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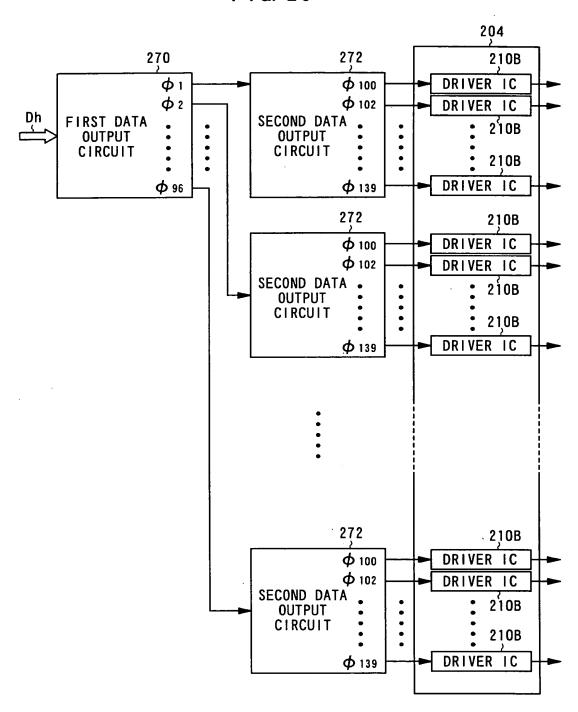
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FIG. 25



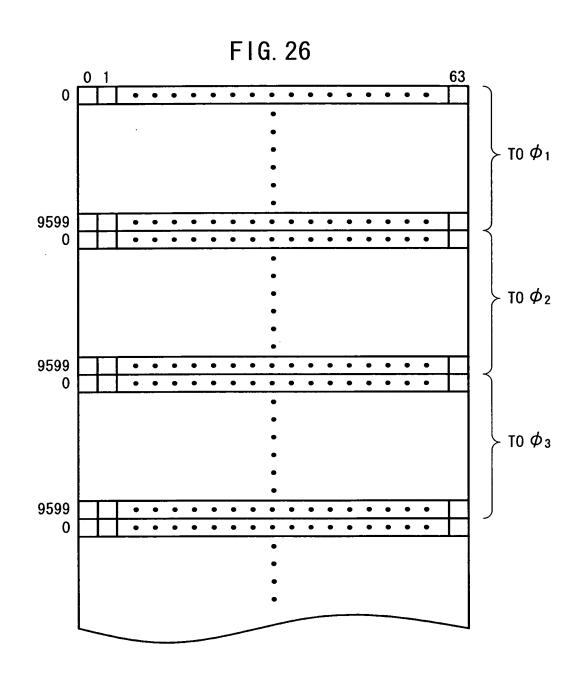


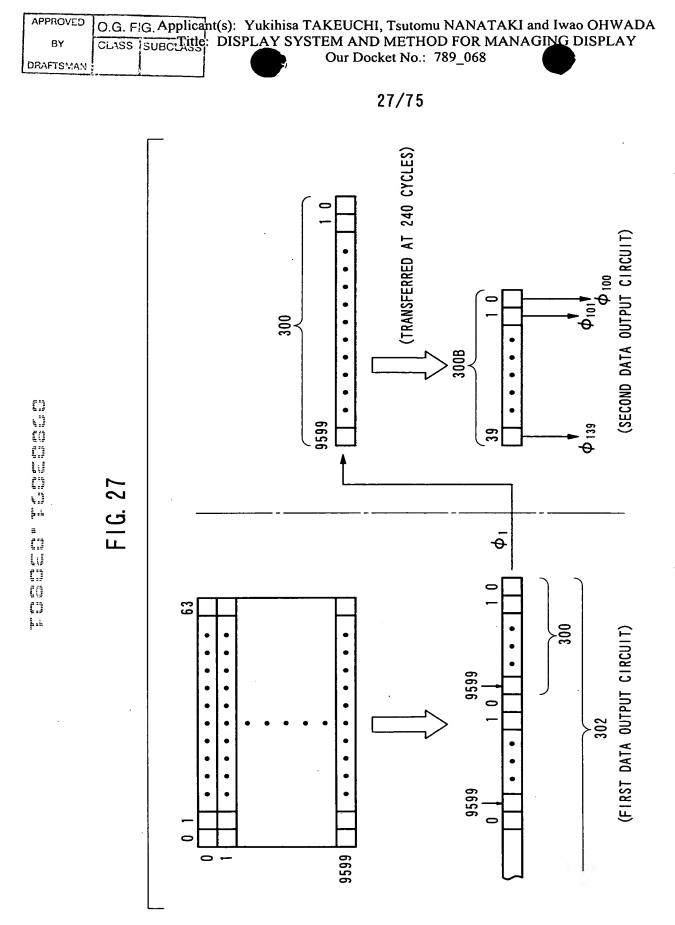
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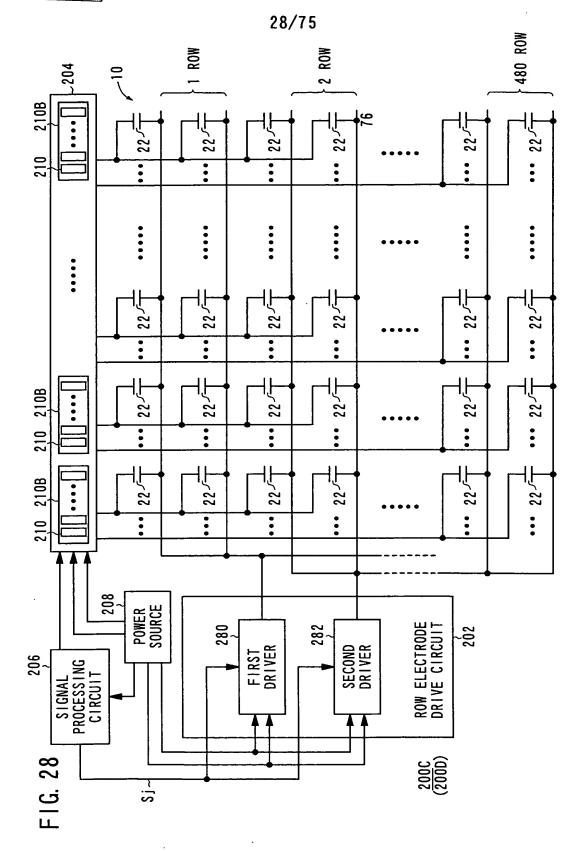
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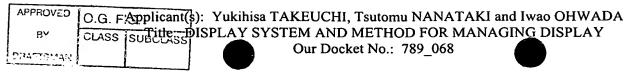
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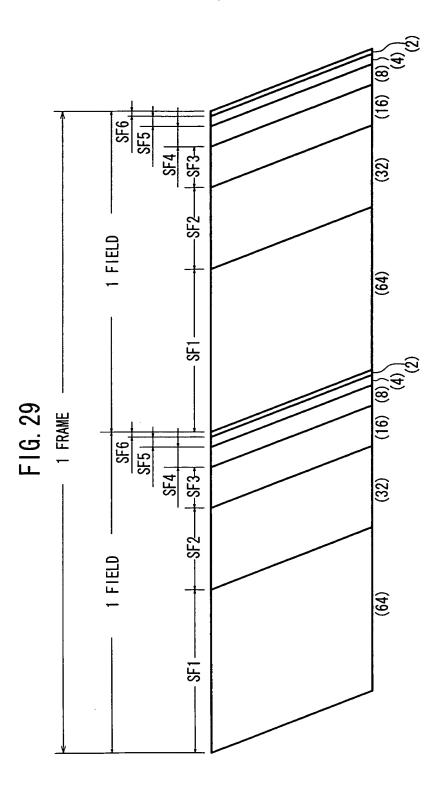




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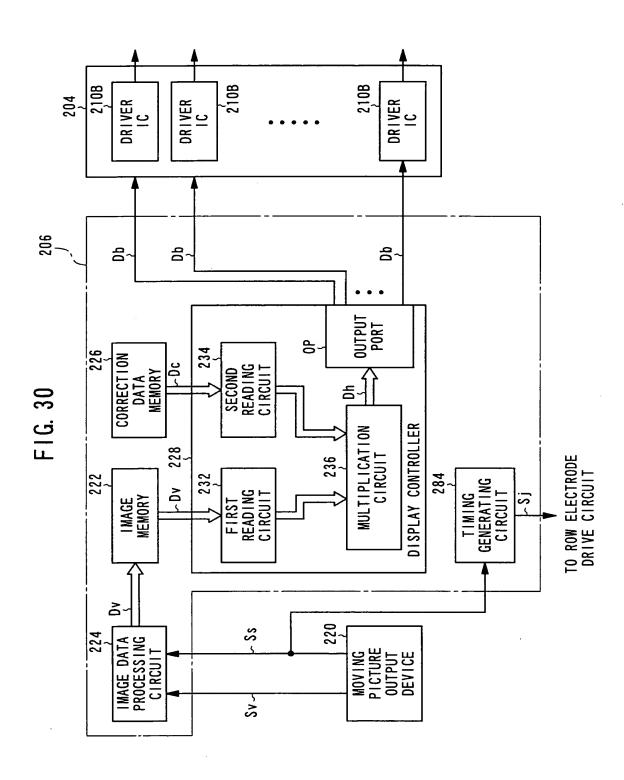
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FIG. 31

		ON SIGNAL	OFF SIGNAL
ļ		OV	60 <b>V</b>
SELECT	10V	-10V	50V
SIGNAL		(LIGHT EMISSION)	(LIGHT OFF)
NONSELECT	-50V	50V	110V
SIGNAL		(LIGHT EMISSION)	(LIGHT OFF)

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O.G. FIG.Title: DISPLAY SYSTEM AND METHOD FOR MANAGING DISPLAY
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FIG. 32

		ON SIGNAL	OFF SIGNAL
		0V	60V
SELECT SIGNAL	ov	ov	60V
NONSELECT SIGNAL	-60V	60V	120V

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FIG. 33

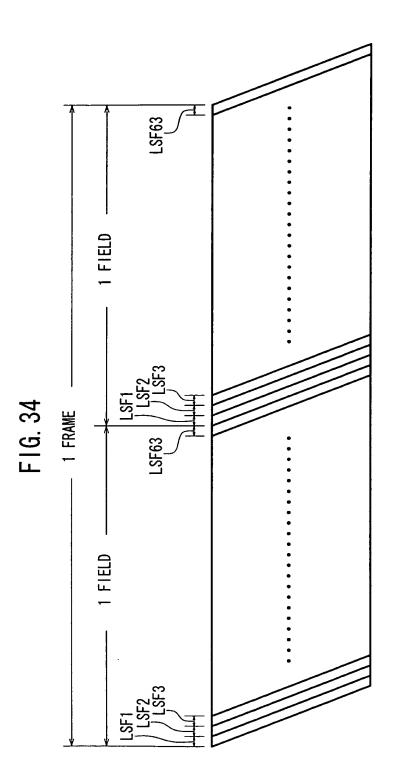
		ON SIGNAL	OFF SIGNAL
		60V	0V
SELECT SIGNAL	50V	-10V	50V
NONSELECT S I GNAL	110V	50V	110V

Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

APPROVED O.G. Fig. Title: DISPLAY SYSTEM AND METHOD FOR MANAGING DISPLAY

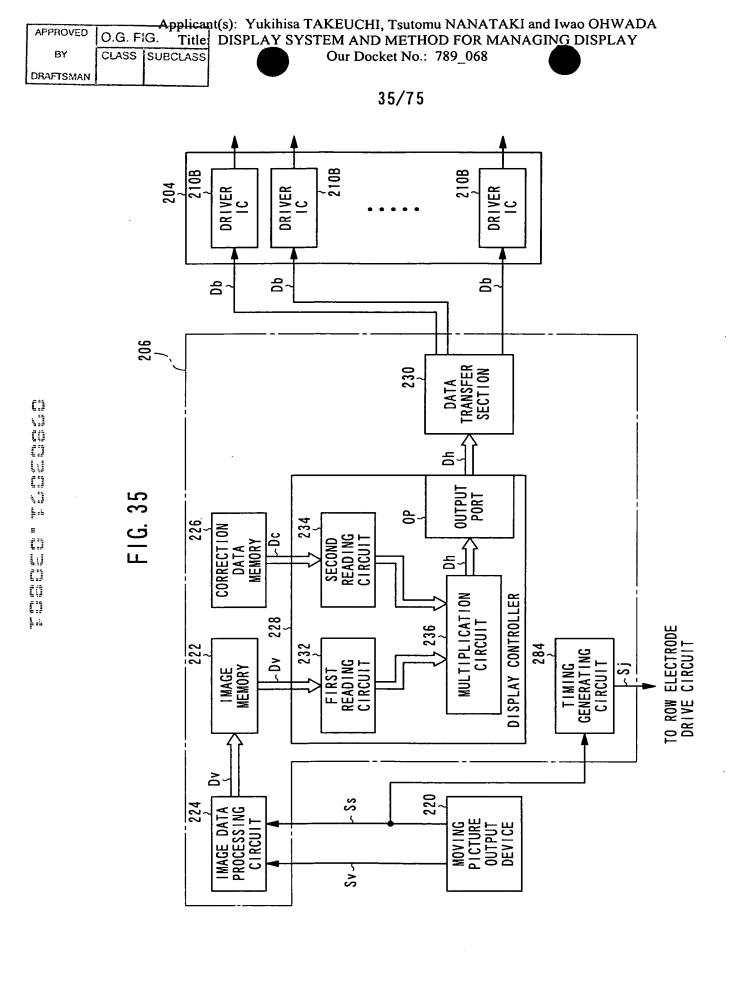
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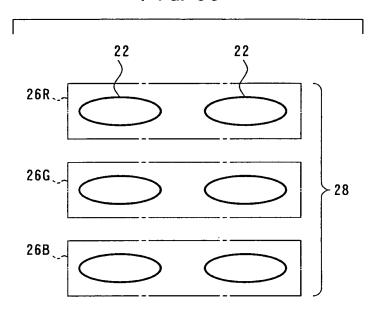
APPHOVED O.G. FIG. Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

Title: DISPLAY SYSTEM AND METHOD FOR MANAGING DISPLAY

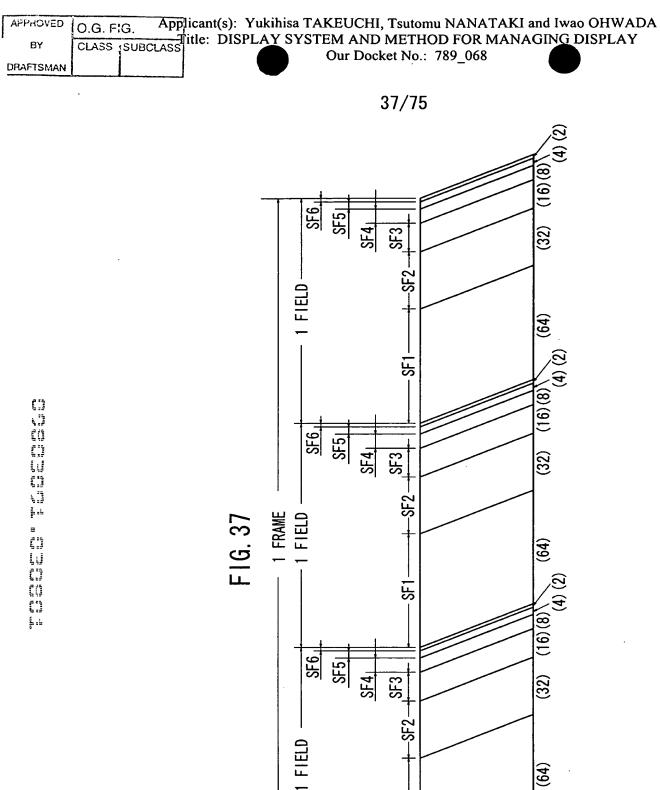
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FIG. 36

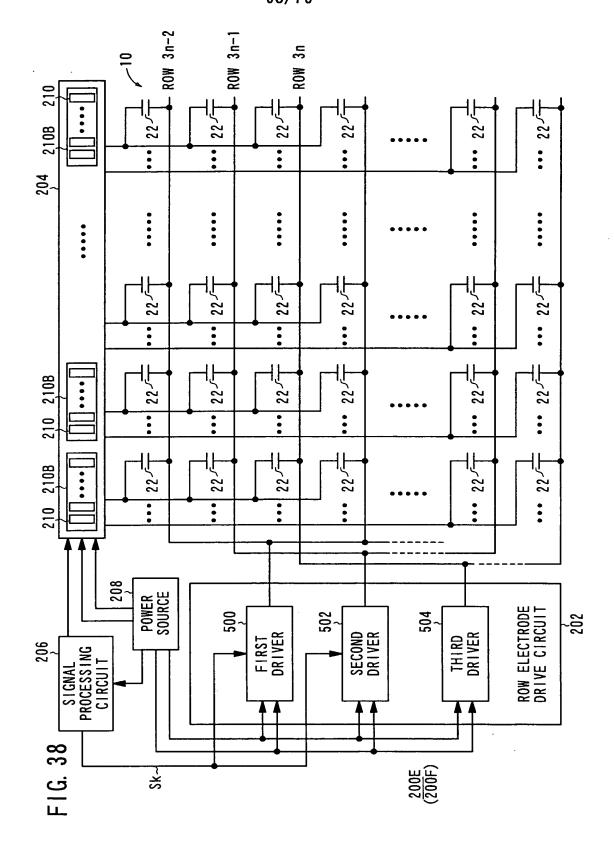


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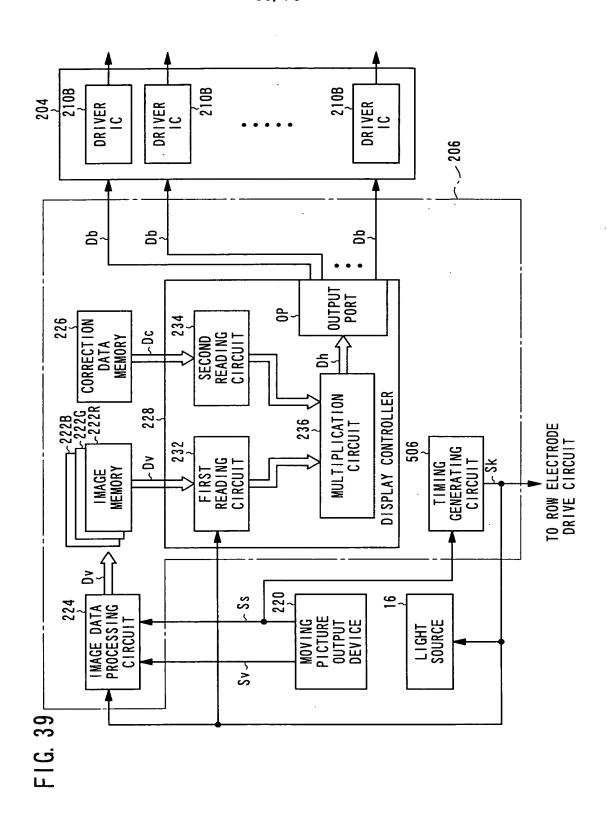


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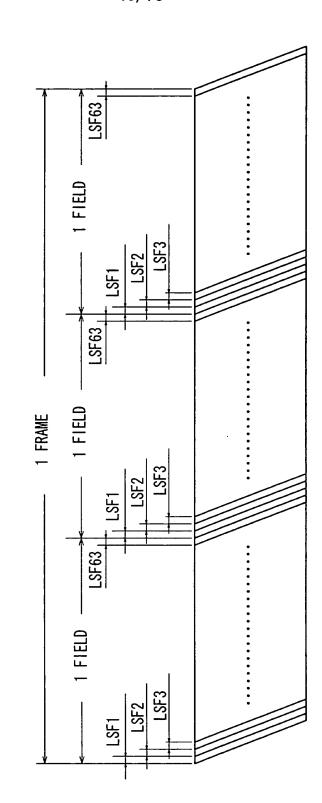


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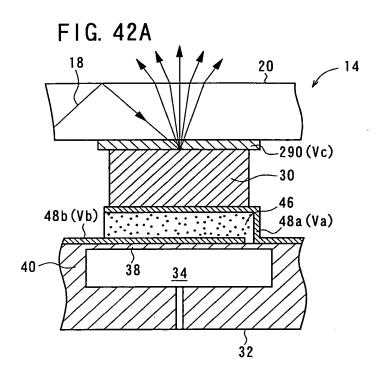
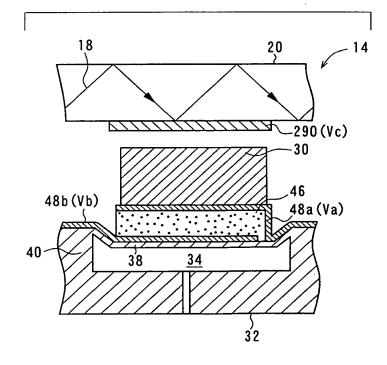


FIG. 42B

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FIG. 43A

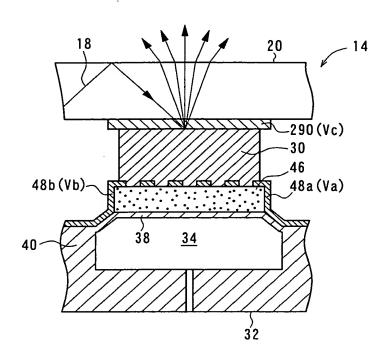
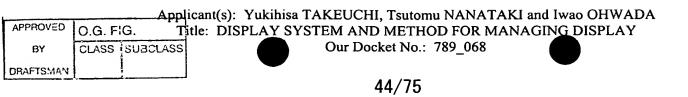


FIG. 43B

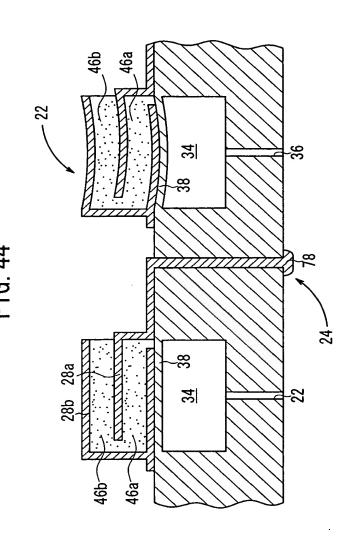
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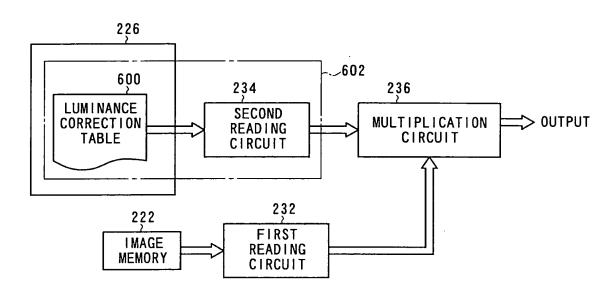


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FIG. 45

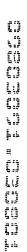


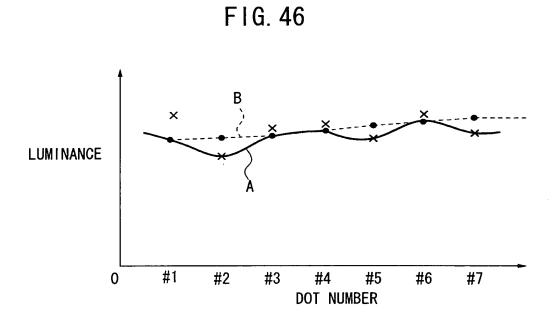
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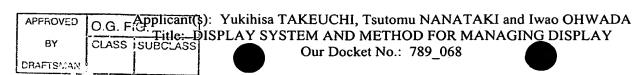
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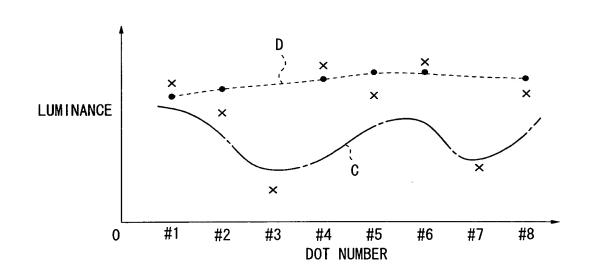




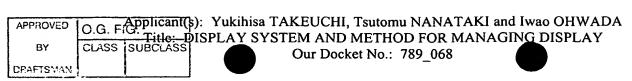


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FIG. 47



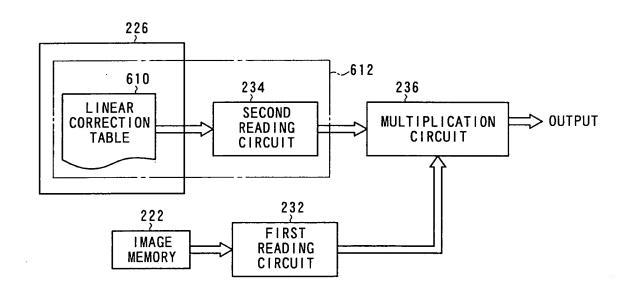
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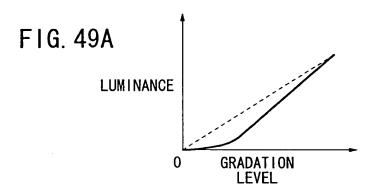
FIG. 48

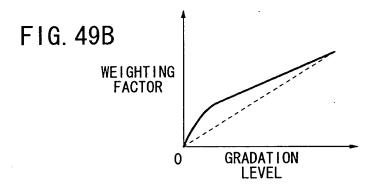


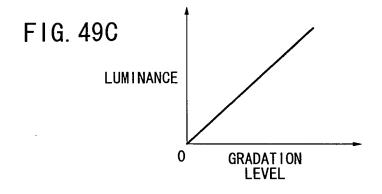
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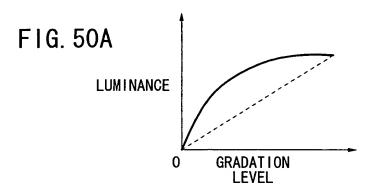
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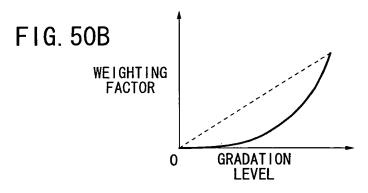


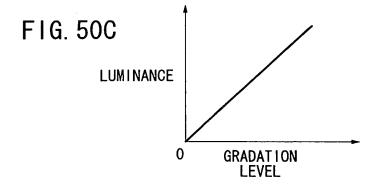




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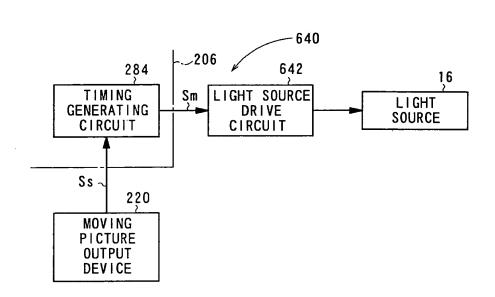


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FIG. 51



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FIG. 52A

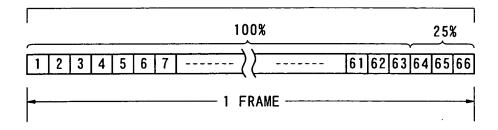
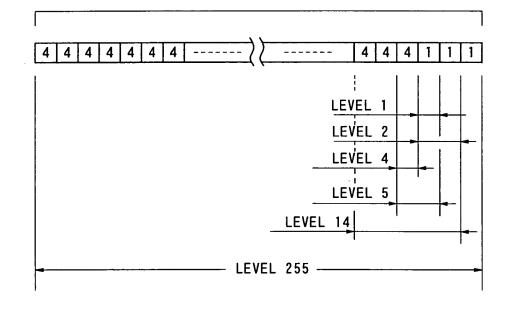


FIG. 52B





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FIG. 53A

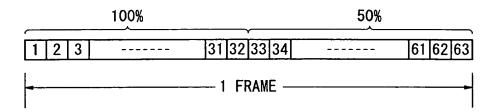
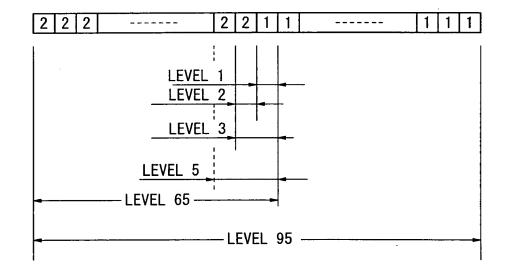


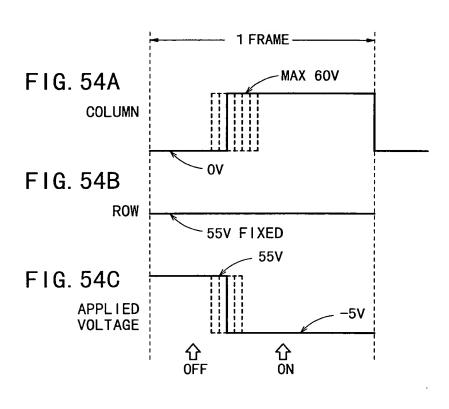
FIG. 53B

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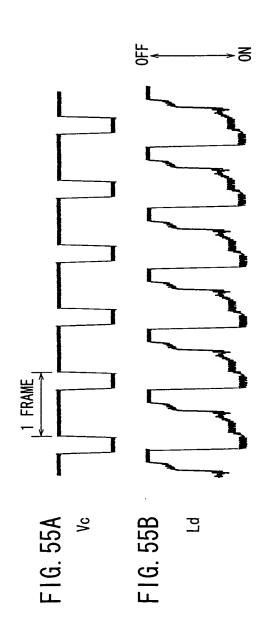


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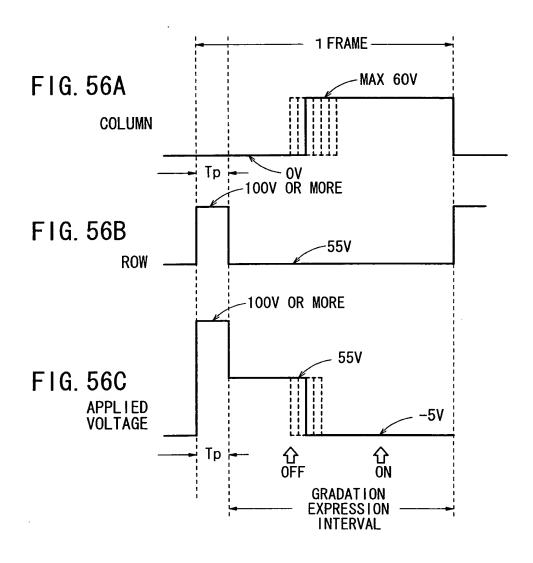




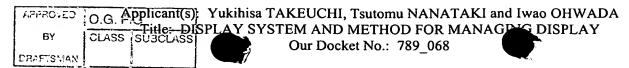




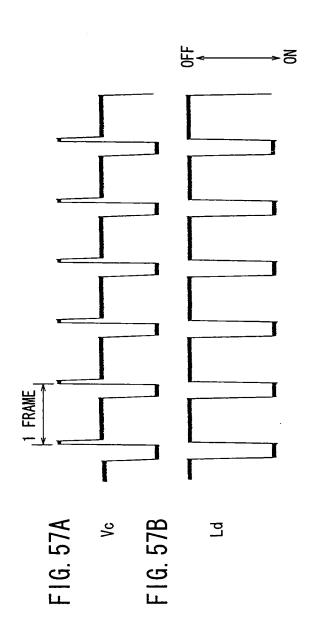
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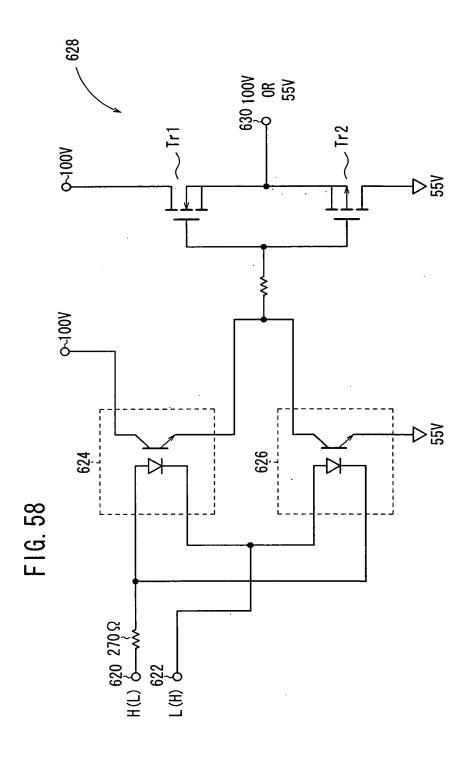
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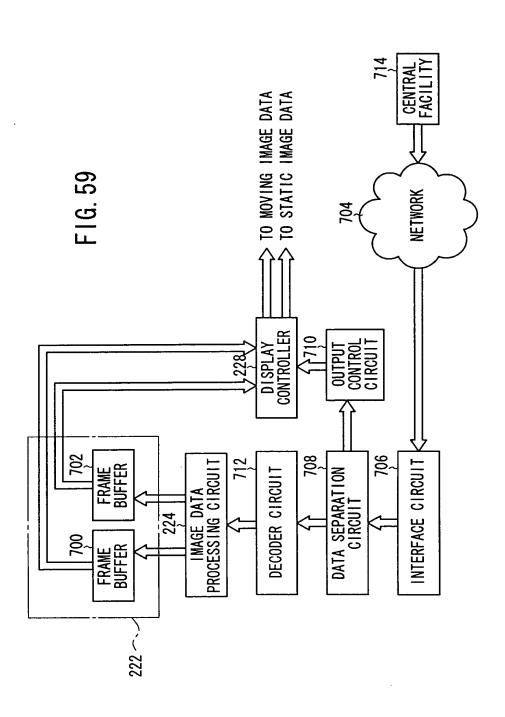


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Applicant(s): Yukihisa TAKEUCHI, Tsutomu NANATAKI and Iwao OHWADA

D.G. FIG.Title: DISPLAY SYSTEM AND METHOD FOR MANAGING DISPLAY

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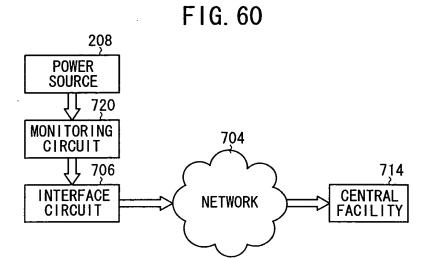
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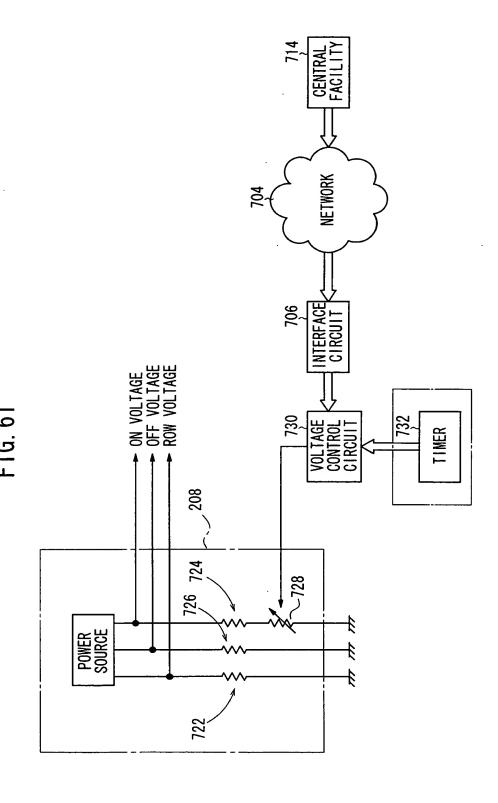
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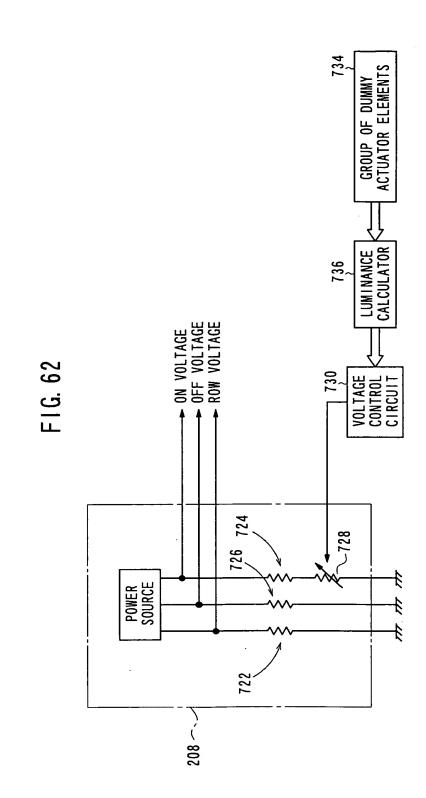


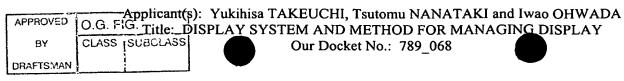
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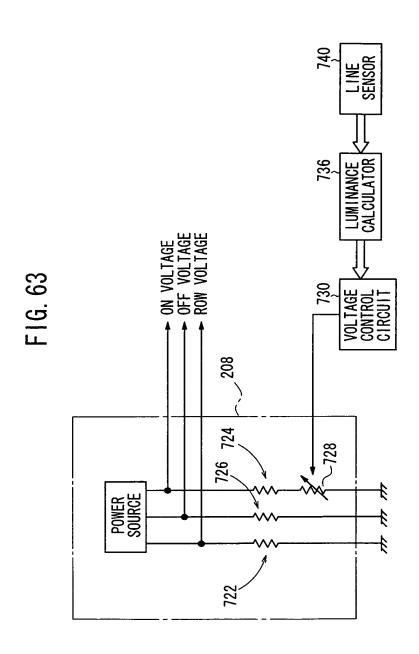
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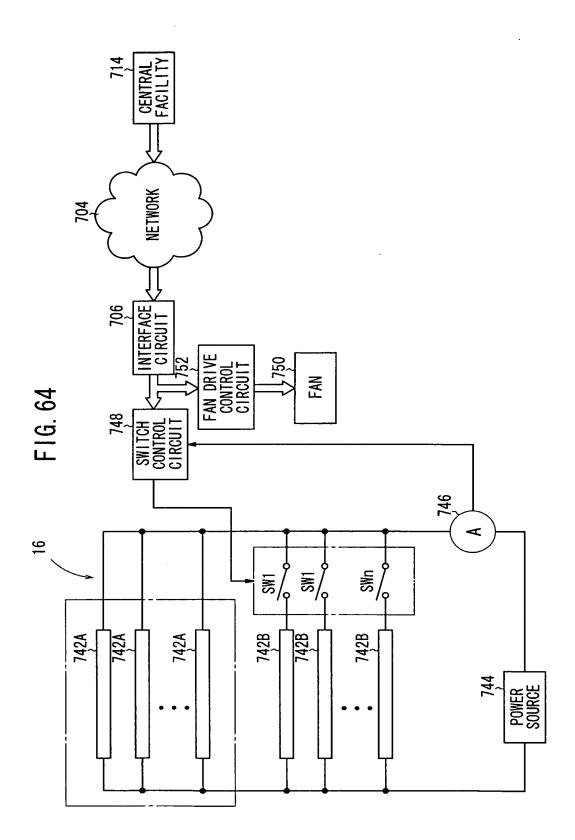
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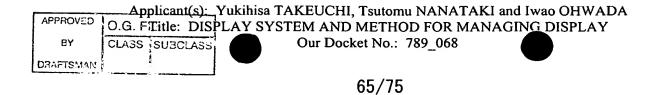
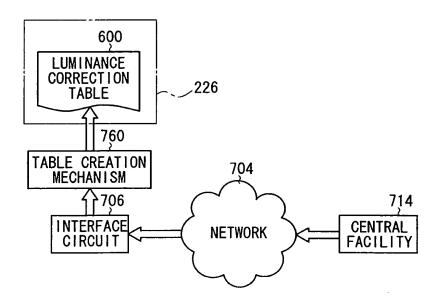
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FIG. 65

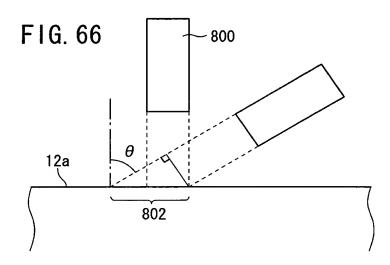


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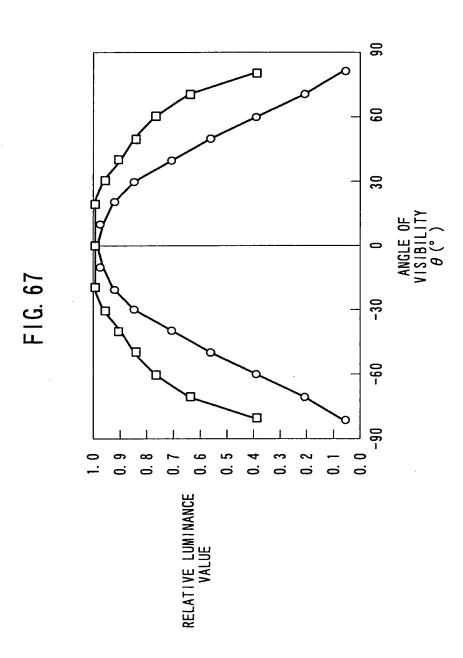


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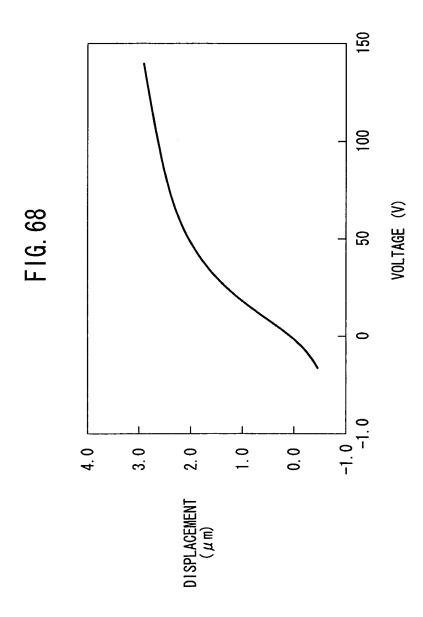
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FIG. 69A

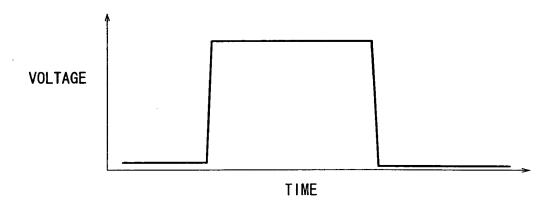
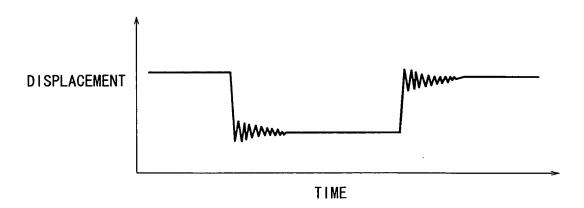


FIG. 69B

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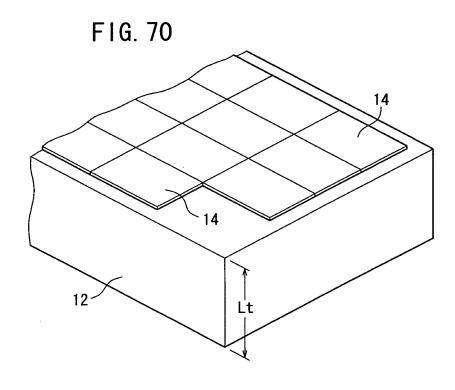
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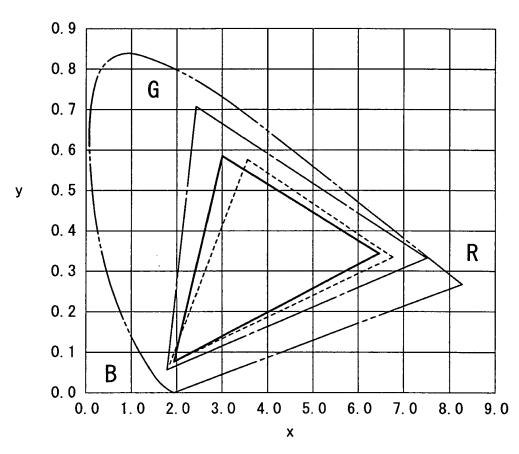
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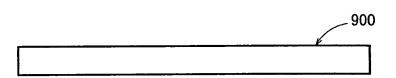
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Our Docket No.: 789\_068

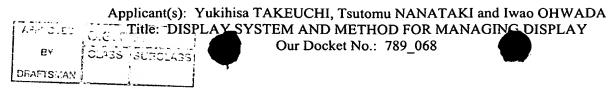
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FIG. 72



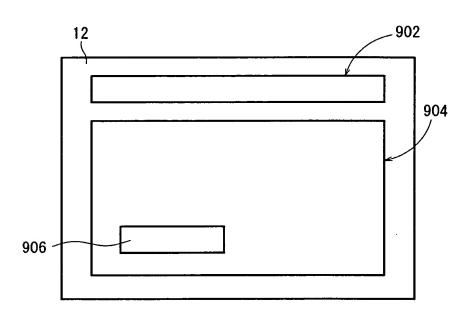
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FIG. 73

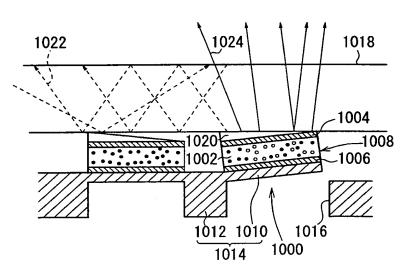


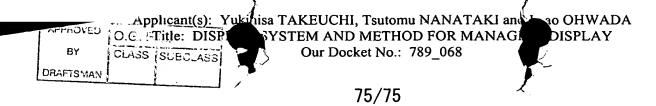
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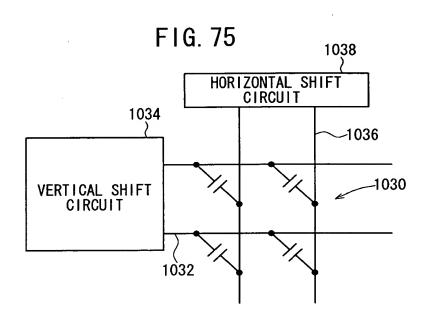
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FIG. 74







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